



Dept.: E/M
By: EHK/STD/kke

OSMM&N File No. 247958US2

✓ Serial No. 10/762,340

In the matter of the Application: Kazunari HATADE, et al.
of:
For: SEMICONDUCTOR DEVICE CAPABLE OF AVOIDING LATCHUP
BREAKDOWN RESULTING FROM NEGATIVE VARIATION OF
FLOATING OFFSET VOLTAGE

Due Date: N/A

The following has been received in the U.S. Patent Office on the date stamped hereon:

- Credit Card Form for \$130.00
- Dep. Acct. Order Form
- Request to Correct Inventorship under 37 CFR 1.48(b)(1)
- PTO Cover Letter
- Submission of Supplemental ADS
- Supplemental ADS

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